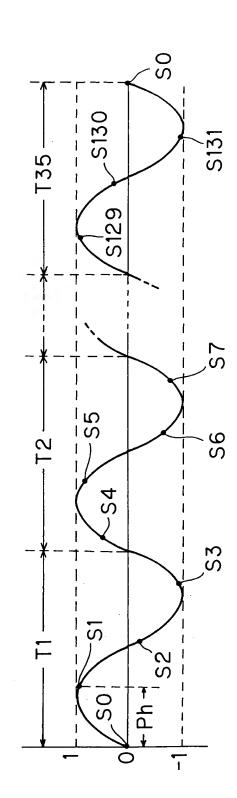
COLLYGID NELECT

Fig.1 PRIOR ART



INVERTING CIRCUIT ည တ 000 ROM a I  $\sim$ Sin Fig.2 R-Y LUMINANCE/COLOR-DIFFERENCE B-Y SIGNAL GENERATING CIRCUIT ADDRESS CALCULATING CIRCUIT Se 9 <u>د</u>

œ



Fig.3

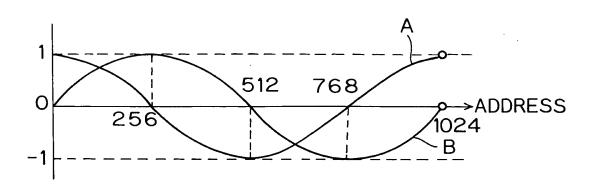


Fig.4

